2nd official Workshop on the Topic



Programmable Processing for the Future and Autonomous Car

- From classical FPGA to heterogeneous MPSoC

26th June 2018 / Hochschule Ulm, 89075 Ulm, Germany

Agenda

08:45 - 09:15 Uhr	Registration
09:15 – 09:30 Uhr	Anestis Terzis (Hochschule Ulm) & Endric Schubert (MLE): Welcome Note
09:30 – 10:00 Uhr	Karsten Trott (Field Applications Engineer, Xilinx): Xilinx reVision xfOpenCV.
10:00 – 11:00Uhr	Key Note: Wolfgang Fey (Senior Manager Assisted Driving Control Systems, Location Manager, Continental Ulm): Automated Driving: A "Quantum Leap" for the next Generation Automotive Systems!
11:00 – 11:30 Uhr	Felix Eberli (Department Head, Supercomputing Systems AG): Smart Automotive Sensors with acceleration on FPGA and CPU.
11:30 – 12:00 Uhr	Dr. Petros Kapsalas (Computer Vision Expert, Panasonic Automotive & Industrial Systems): Supervised versus Unsupervised SLAM.
12:00 – 12:30 Uhr	Jochen Frensch (Lead Engineer, Mentor Embedded, Mentor Graphics Development (Deutschland) GmbH): Implementation of a fully connected feed forward neural network using Vivado HLS.
12:30 - 13:30 Uhr	Lunch / Networking
12:30 – 13:30 Uhr 13:30 – 14:00 Uhr	
	Lunch / Networking Mark Leznik (Researcher, Institute of Information Resource Management, Ulm University):
13:30 – 14:00 Uhr	Lunch / Networking Mark Leznik (Researcher, Institute of Information Resource Management, Ulm University): Is the Cloud Ready for Autonomous Driving? Philipp Hoffmann (Project Manager Interior Concepts Entry Class Vehicles, BMW Group):
13:30 – 14:00 Uhr 14:00 – 14:30 Uhr	Lunch / Networking Mark Leznik (Researcher, Institute of Information Resource Management, Ulm University): Is the Cloud Ready for Autonomous Driving? Philipp Hoffmann (Project Manager Interior Concepts Entry Class Vehicles, BMW Group): Concepts and Image Processing with Multi-Camera-Monitor-Systems.
13:30 – 14:00 Uhr 14:00 – 14:30 Uhr 14:30 – 15:00 Uhr	Lunch / Networking Mark Leznik (Researcher, Institute of Information Resource Management, Ulm University): Is the Cloud Ready for Autonomous Driving? Philipp Hoffmann (Project Manager Interior Concepts Entry Class Vehicles, BMW Group): Concepts and Image Processing with Multi-Camera-Monitor-Systems. Coffee Break / Networking Steffen Jannik Maier (Researcher, Institute of Communication Technology, Hochschule Ulm):

Program Committee Prof. Dr. A. Terzis, Dr. E. Schubert, M. Güthoff **Conference Language** German and English **Contact** Prof. Dr. A. Terzis, Eberhard-Finckh-Straße 11, 89075 Ulm, Fon: +49 (0731) 50-28341, terzis@hs-ulm.de







2nd official Workshop on the Topic



Programmable Processing for the Future and Autonomous Car

- From classical FPGA to heterogeneous MPSoC

26th June 2018 / Hochschule Ulm, 89075 Ulm, Germany

Registration:

The fee for this Workshop is 50 € and includes workshop documentation and lunch. The participation for students and employees of the Hochschule Ulm is free of charge. The payment will be directly on site.

Seating is limited. Please register via email with your name, contact info and company affiliation that we can reserve a seat for you. Please use the following email address to register:

fpga2018@mlecorp.com



In case of administrative or organizational questions please contact Diana Gnensch: +49 1511 511 86 52.

The Workshop will take place:

Hochschule Ulm Campus Albert-Einstein-Allee Albert-Einstein-Allee 55 89081 Ulm Room Q029



Directions:

Please click the following link to find the location:

https://studium.hs-ulm.de/de/Seiten/Standorte.aspx

This Workshop will be located at Campus Albert-Einstein-Allee, Location B.

Program Committee Prof. Dr. A. Terzis, Dr. E. Schubert, M. Güthoff **Conference Language** German and English **Contact** Prof. Dr. A. Terzis, Eberhard-Finckh-Straße 11, 89075 Ulm, Fon: +49 (0731) 50-28341, terzis@hs-ulm.de





